

Docket No.: 57454-132

#2
9-14-01
JM
PATENT

1-903 U.S. PTO
09/874239
06/06/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Akira YAMADA

Serial No.:

Group Art Unit:

Filed: June 6, 2001

Examiner:

For: SYNCHRONOUS SIGNAL PRODUCING CIRCUIT FOR CONTROLLING A DATA
READY SIGNAL INDICATIVE OF END ACCESS TO A SHARED MEMORY AND
THEREBY CONTROLLING SYNCHRONIZATION BETWEEN PROCESSOR AND
COPROCESSOR

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, DC 20231

Dear Sir:

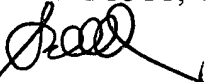
In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits.

No certification or fee is required.

Respectfully submitted,

MCDERMOTT, WILL & EMERY


Stephen A. Becker
Registration No. 26,527

600 13th Street, N.W.
Washington, DC 20005-3096
(202) 756-8000 SAB:ykg
Date: June 6, 2001
Facsimile: (202) 756-8087